P6495a

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Yoshiaki Mori, et al. Group Art Unit:

1796

Serial No.:

10/026,286

Examiner:

Not Yet Assigned

Filed:

December 20, 2001

PATTERN FORMING METHOD AND APPARATUS USED FOR SEMICONDUCTOR DEVICE, ELECTRIC CIRCUIT, DISPLAY MODULE, AND LIGHT EMITTING DEVICE

CERTIFICATE OF MAILING

fereby certify that this correspondence, and the documents referred to as attached herein, are being deposited with the United States Postal Service on this date in an envelope as "First Class Mail" service addressed to Box PGPUB DRAWINGS, Assistant Commissioner for Patents, Washington, DC 20231.

Date: May 31, 2002

SUBMISSION OF FORMAL DRAWINGS AND

PETITION FOR EXTENSION OF TIME (IF REQUIRED)

Box: PGPUB DRAWINGS

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Enclosed is a set of formal drawings consisting of 23 sheets. Please enter these drawings in the application so that it may be published.

Applicants believe that no extension of time is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time. Commissioner is hereby authorized to charge payment of the fee for such extension of time to our Deposit Account No. 19-2746.

Respectfully submitted,

Mark P. Watson

Registration No. 31,448

Please address all correspondence to: Epson Research and Development, Inc. Intellectual Property Department 150 River Oaks Parkway, Suite 225 San Jose, CA 95134 Customer No. 20178

Phone: (408) 952-6000 Fax: (408) 954-9058

Date: May 31, 2002